

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Atsushi YOSHIKAWA et al.
Title: DELAY ADJUSTMENT CIRCUIT
AND A CLOCK GENERATING
CIRCUIT USING THE SAME
Appl. No.: 09/773,595
Filing Date: 02/02/2001
Examiner: T. N. Du
Art Unit: 2116

AMENDMENT AND REPLY UNDER 37 CFR 1.111

Mail Stop Amendment
5 Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

10 Sir:

This communication is responsive to the Non-Final Office Action dated February 18, 2004, concerning the above-referenced patent application.

Applicant has enclosed with this amendment a Petition for Extension of Time to make this response timely.

15 **Remarks/Arguments** begin on page 8 of this document.

Please amend the application as follows: